## **Rectification** Question paper

| Level      | International A Level |  |  |  |  |
|------------|-----------------------|--|--|--|--|
| Subject    | Physics               |  |  |  |  |
| Exam Board | CIE                   |  |  |  |  |
| Торіс      | Alternating Currents  |  |  |  |  |
| Sub Topic  | Rectification         |  |  |  |  |
| Paper Type | Theory                |  |  |  |  |
| Booklet    | Question paper        |  |  |  |  |

| Time Allowed: | 90 minutes |
|---------------|------------|
| Score:        | /75        |
| Percentage:   | /100       |

## <u>CHEMISTRY ONLINE</u>

| A*   | Α      | В   | С     | D     | E   | U    |
|------|--------|-----|-------|-------|-----|------|
| >85% | '77.5% | 70% | 62.5% | 57.5% | 45% | <45% |

1 (a) State Faraday's law of electromagnetic induction.

(b) The output of an ideal transformer is connected to a bridge rectifier, as shown in Fig. 6.1.





The input to the transformer is 240 V r.m.s. and the **maximum** potential difference across the load resistor is 9.0 V.

- (i) On Fig. 6.1, mark with the letter P the positive output from the rectifier. [1]
- (ii) Calculate the ratio

number of turns on primary coil number of turns on secondary coil

ratio = ......[3]

(c) The variation with time *t* of the potential difference *V* across the load resistor in (b) is shown in Fig. 6.2.





A capacitor is now connected in parallel with the load resistor to produce some smoothing.

(i) Explain what is meant by smoothing.

......[1]

(ii) On Fig. 6.2, draw the variation with time *t* of the smoothed output potential difference. [2]

2 A bridge rectifier consists of four ideal diodes A, B, C and D, connected as shown in Fig. 6.1.



An alternating supply is applied between the terminals X and Y.

- (a) (i) On Fig. 6.1, label the positive (+) connection to the load resistor R. [1]
  - (ii) State which diodes are conducting when terminal Y of the supply is positive.

diode .....[1]

(b) The variation with time t of the potential difference V across the load resistor R is shown in Fig. 6.2.



Fig. 6.2

The load resistor R has resistance  $2700 \Omega$ .

(i) Use Fig. 6.2 to determine the mean power dissipated in the resistor R.

power = ..... W [3]

- (ii) On Fig. 6.1, draw the symbol for a capacitor, connected so as to increase the mean power dissipated in the resistor R. [1]
- (c) The capacitor in (b)(ii) is now removed from the circuit. The diode A in Fig. 6.1 stops functioning, so that it now has infinite resistance.

On Fig. 6.2, draw the variation with time t of the new potential difference across the resistor R. [2]



**3** A sinusoidal alternating voltage supply is connected to a bridge rectifier consisting of four ideal diodes. The output of the rectifier is connected to a resistor R and a capacitor C as shown in Fig. 6.1.



The function of C is to provide some smoothing to the potential difference across R. The variation with time t of the potential difference V across the resistor R is shown in Fig. 6.2.





(a) Use Fig. 6.2 to determine, for the alternating supply,

(ii) the root-mean-square (r.m.s.) voltage,

r.m.s. voltage = ..... V [1]

(i)

(iii) the frequency. Show your working.

frequency = ..... Hz [2]

- (b) The capacitor C has capacitance  $5.0 \,\mu$ F. For a single discharge of the capacitor through the resistor R, use Fig. 6.2 to
  - (i) determine the change in potential difference,

change = ...... V [1]

(ii) determine the change in charge on each plate of the capacitor,

change = ..... C [2]

(iii) show that the average current in the resistor is  $1.1 \times 10^{-3}$  A.

(c) Use Fig. 6.2 and the value of the current given in (b)(iii) to estimate the resistance of resistor R.



4 The components for a bridge rectifier are shown in Fig. 5.1.



- **5** A sinusoidal alternating voltage is to be rectified.
  - (a) Suggest one advantage of full-wave rectification as compared with half-wave rectification.

(b) The rectification is produced using the circuit of Fig. 7.1.





All the diodes may be considered to be ideal.

The variation with time t of the alternating voltage applied to the circuit is shown in Fig. 7.2 and in Fig. 7.3.







- (i) On the axes of Fig. 7.2, draw a graph to show the variation with time *t* of the potential difference across diode A. [1]
- (ii) On the axes of Fig. 7.3, draw a graph to show the variation with time *t* of the potential difference across diode B. [1]
- (c) (i) On Fig. 7.1, draw the symbol for a capacitor, connected into the circuit so as to provide smoothing. [1]
  - (ii) Fig. 7.4 shows the variation with time *t* of the smoothed potential difference across the resistor R in Fig. 7.1.



1. State how the amount of smoothing may be increased.

[1]

2. On Fig. 7.4, draw the variation with time *t* of the potential difference across resistor R for increased smoothing. [2]



- 6 An ideal transformer has 5000 turns on its primary coil. It is to be used to convert a mains supply of 230 V r.m.s. to an alternating voltage having a peak value of 9.0 V.
  - (a) Calculate the number of turns on the secondary coil.



(b) The output from the transformer is to be full-wave rectified. Fig. 4.1 shows part of the rectifier circuit.



On Fig. 4.1, draw

- (i) diode symbols to complete the diagram of the rectifier such that terminal A of the resistor R is positive with respect to terminal B, [2]
- (ii) the symbol for a capacitor connected to provide smoothing of the potential difference across the resistor R. [1]

(c) Fig. 4.2 shows the variation with time *t* of the smoothed potential difference *V* across the resistor R.



(i) State the interval of time during which the capacitor is being charged from the transformer.

(ii) The resistance of the resistor R is doubled. On Fig. 4.2, sketch the variation with time *t* of the potential difference *V* across the resistor. [2]



7 The rectified output of a sinusoidal signal generator is connected across a resistor **R** of resistance 1.5 k, as shown in Fig. 4.1.





The variation with time t of the potential difference V across  $\mathbf{R}$  is shown in Fig. 4.2.





(a) State how the rectification shown in Fig. 4.2 may be achieved.

(b) A capacitor is now connected in parallel with the resistor **R**. The resulting variation with time *t* of the potential difference *V* across **R** is shown in Fig. 4.3.



Fig. 4.3

- (i) Using Fig. 4.3, determine
  - 1. the mean potential difference across the resistor R,

potential difference = ..... V

2. the mean current in the resistor,

mean current = ..... A

**3.** the time in each cycle during which the capacitor discharges through the resistor.

- (ii) Using your answers in (i), calculate
  - 1. the charge passing through the resistor during one discharge of the capacitor,

charge = ..... C

2. the capacitance of the capacitor.

capacitance = ..... F [4]

(c) A second capacitor is now connected in parallel with the resistor R and the first capacitor. On Fig. 4.3, draw a line to show the variation with time t of the potential difference V across the resistor.
[1]

8 A student is asked to design a circuit by which a direct voltage of peak value 9.0V is obtained from a 240V alternating supply.

The student uses a transformer that may be considered to be ideal and a bridge rectifier incorporating four ideal diodes.

The partially completed circuit diagram is shown in Fig. 6.1.





- (a) On Fig. 6.1, draw symbols for the four diodes so as to produce the polarity across the load as shown on the diagram. [2]
- (b) Calculate the ratio

number of turns on the secondary coil number of turns on the primary coil



**9** An alternating supply of frequency 50 Hz and having an output of 6.0 V r.m.s. is to be rectified so as to provide direct current for a resistor R. The circuit of Fig. 6.1 is used.



The diode is ideal. The Y-plates of a cathode-ray oscilloscope (c.r.o.) are connected between points A and B.

(a) (i) Calculate the maximum potential difference across the diode during one cycle.

potential difference = ...... V [2] (ii) State the potential difference across R when the diode has maximum potential difference across it. Give a reason for your answer. 

(b) The Y-plate sensitivity of the c.r.o. is set at 2.0 V cm<sup>-1</sup> and the time-base at 5.0 ms cm<sup>-1</sup>.
 On Fig. 6.2, draw the waveform that is seen on the screen of the c.r.o. [3]



Fig. 6.2

- (c) A capacitor of capacitance  $180 \,\mu\text{F}$  is connected into the circuit to provide smoothing of the potential difference across the resistor R.
  - (i) On Fig. 6.1, show the position of the capacitor in the circuit. [1]
  - (ii) Calculate the energy stored in the fully-charged capacitor.